<table>
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<th>Course code</th>
<th>Course Name</th>
<th>L-T-P - Credits</th>
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<td>CS234</td>
<td>DIGITAL SYSTEMS LAB</td>
<td>0-0-3-1</td>
<td>2016</td>
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**Pre-requisite:** CS203 Switching theory and logic design

**Course Objectives:**
1. To familiarize students with digital ICs, the building blocks of digital circuits
2. To provide students the opportunity to set up different types of digital circuits and study their behaviour

**List of Exercises/Experiments:** (minimum 12 exercises/experiments are mandatory)
1. Familiarizations and verification of the truth tables of basic gates and universal gates.
2. Verification of Demorgan's laws for two variables.
3. Implementation of half adder and full adder circuits using logic gates.
4. Implementation of half subtractor and full subtractor circuits using logic gates.
5. Implementation of parallel adder circuit.
6. Realization of 4 bit adder/subtractor and BCD adder circuits using IC 7483.
8. Design and implementation of code convertor circuits
   a) BCD to excess 3 code    b) binary to gray code
9. Implementation of multiplexer and demultiplexer circuits using logic gates. Familiarization with various multiplexer and demultiplexer ICs.
10. Realization of combinational circuits using multiplexer/demultiplexer ICs.
11. Implementation of SR, D, JK, JK master slave and T flip flops using logic gates. Familiarization with IC 7474 and IC 7476.
14. Realization of asynchronous counters using flip flop ICs.
15. Realization of synchronous counters using flip flop ICs. Familiarization with various counter Integrated Circuits.
16. Implementation of a BCD to 7 segment decoder and display.
17. Simulation of Half adder, Full adder using VHDL.
   *Note: The experiments may be done using hardware components and/or VHDL*

**Course outcome:**
Students will be able to:
1. identify and explain the digital ICs and their use in implementing digital circuits.
2. design and implement different kinds of digital circuits.